

Fig. 1

2/12

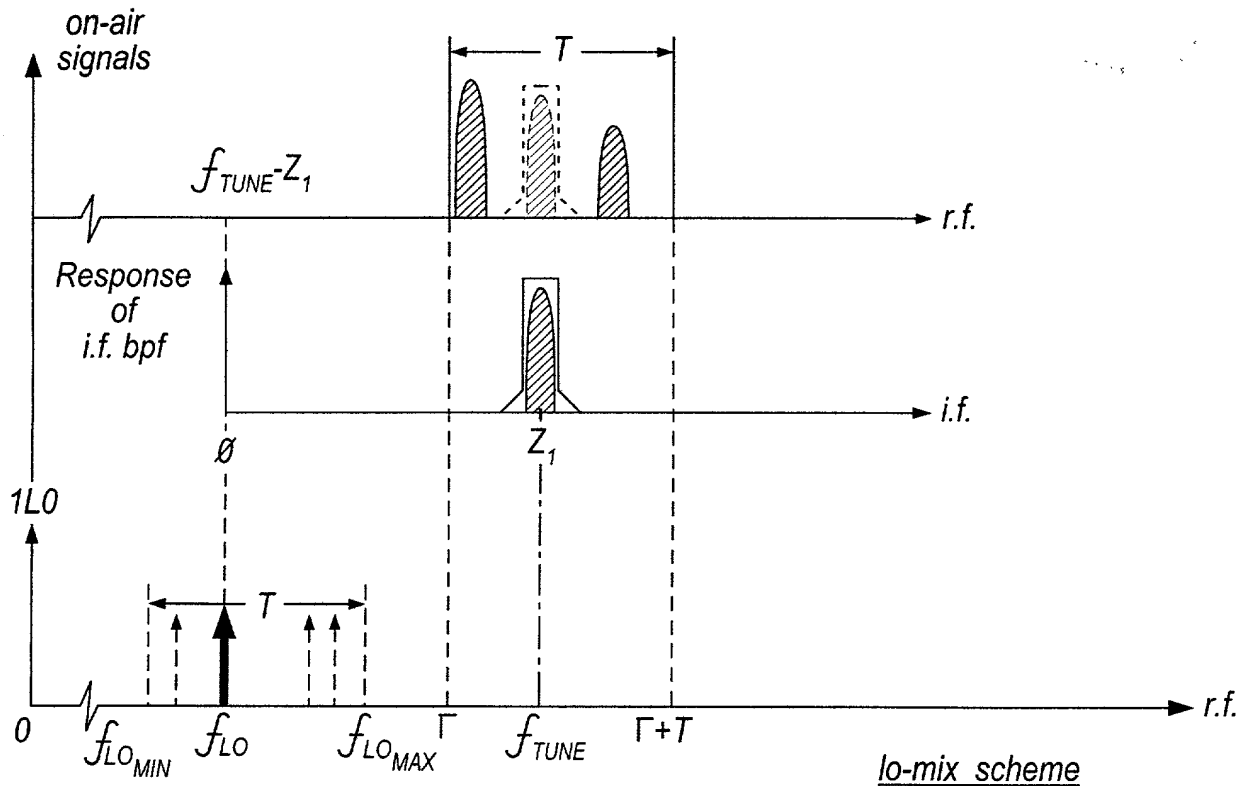


Fig.2a

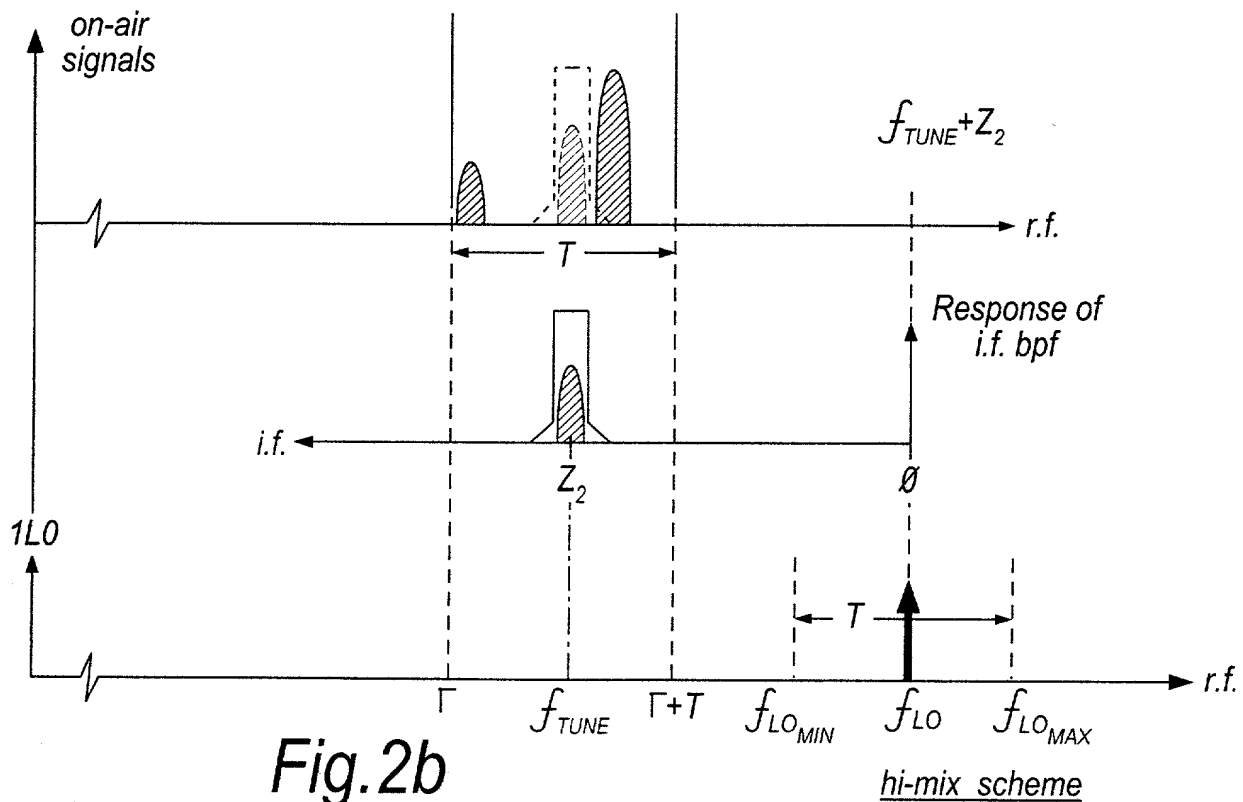


Fig.2b

3/12

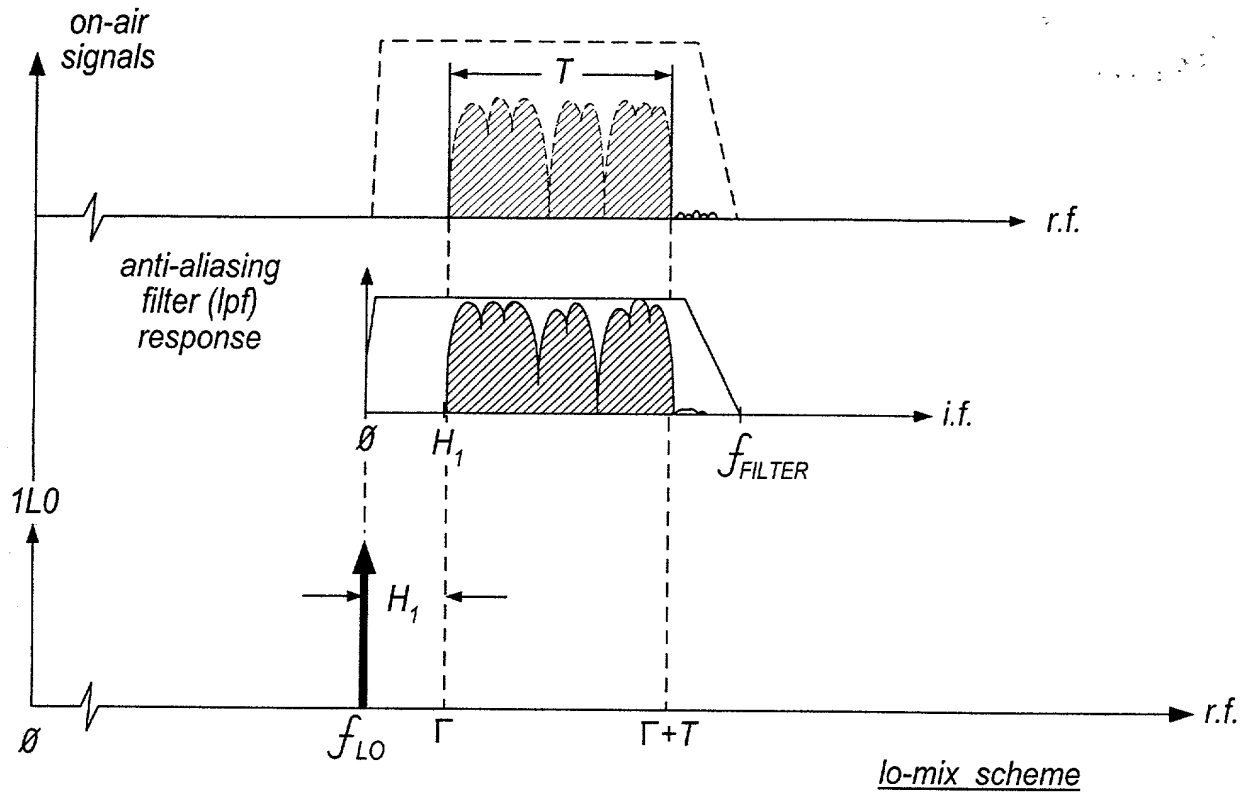


Fig.3a

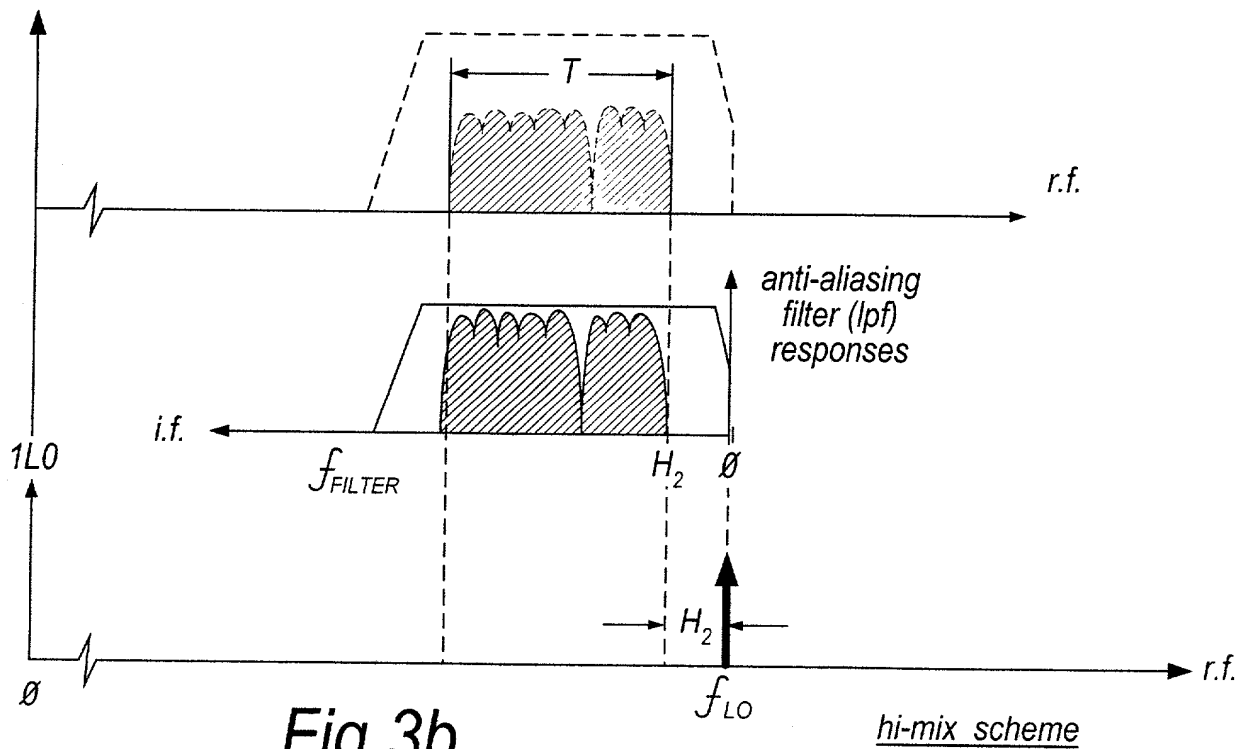


Fig.3b

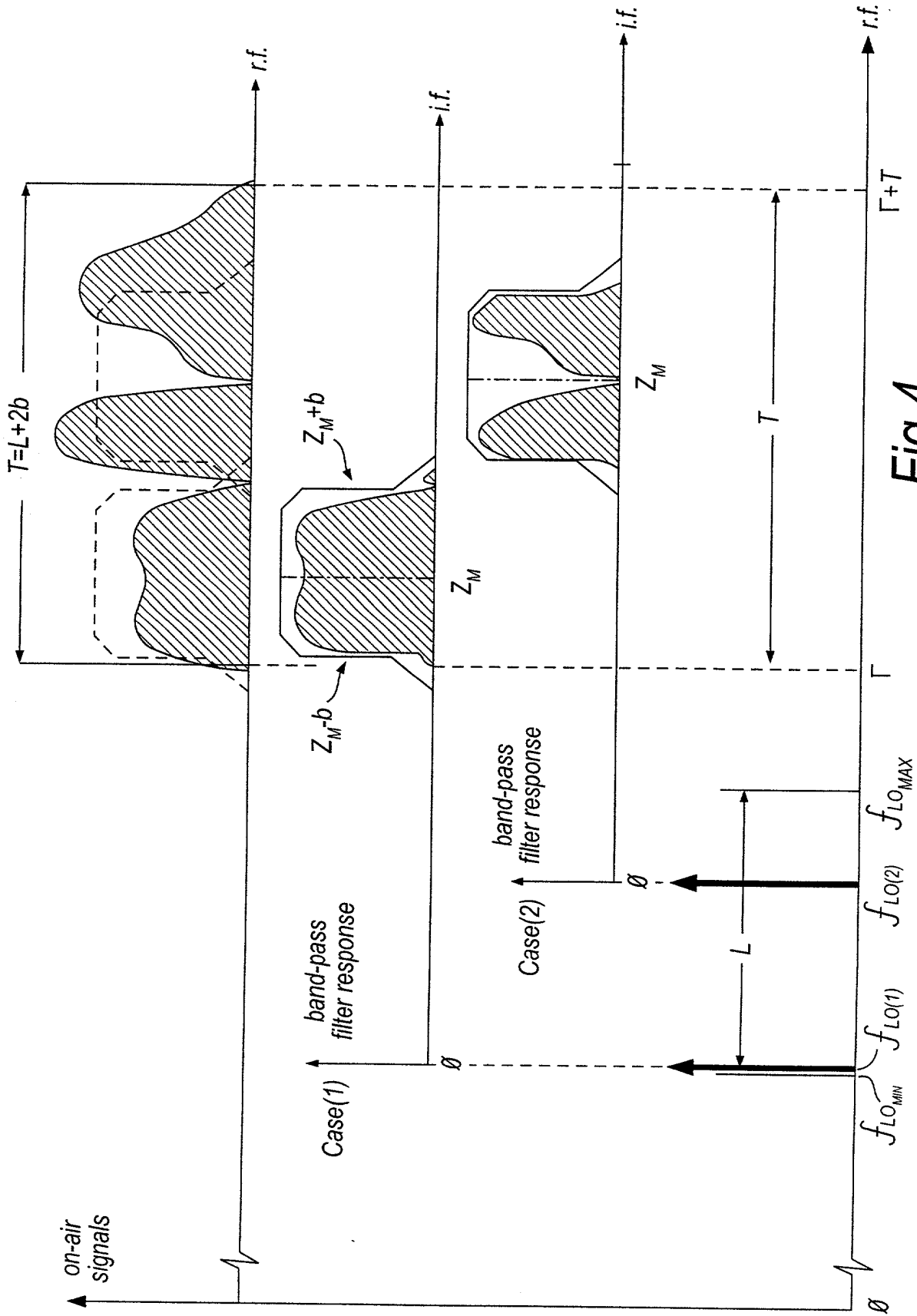


Fig.4

5/12

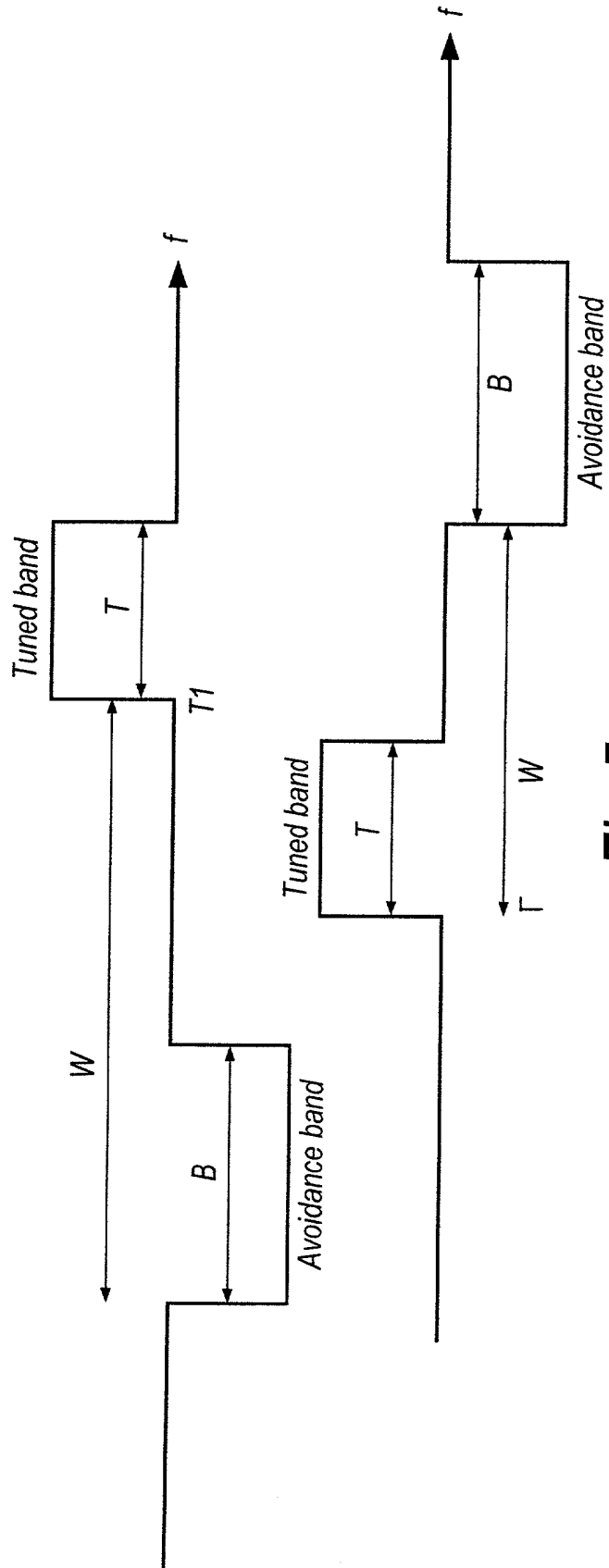


Fig.5

6/12

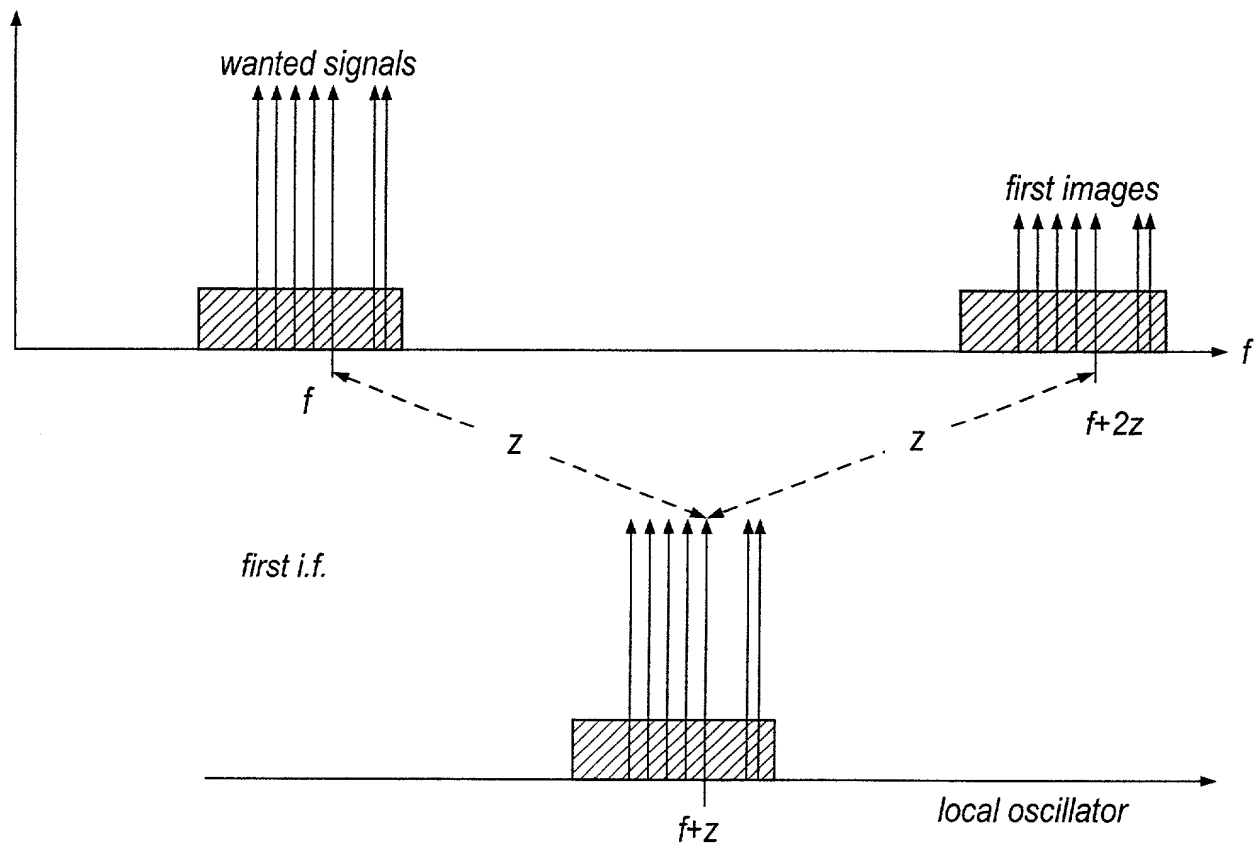


Fig.6

First Image Band

7/12

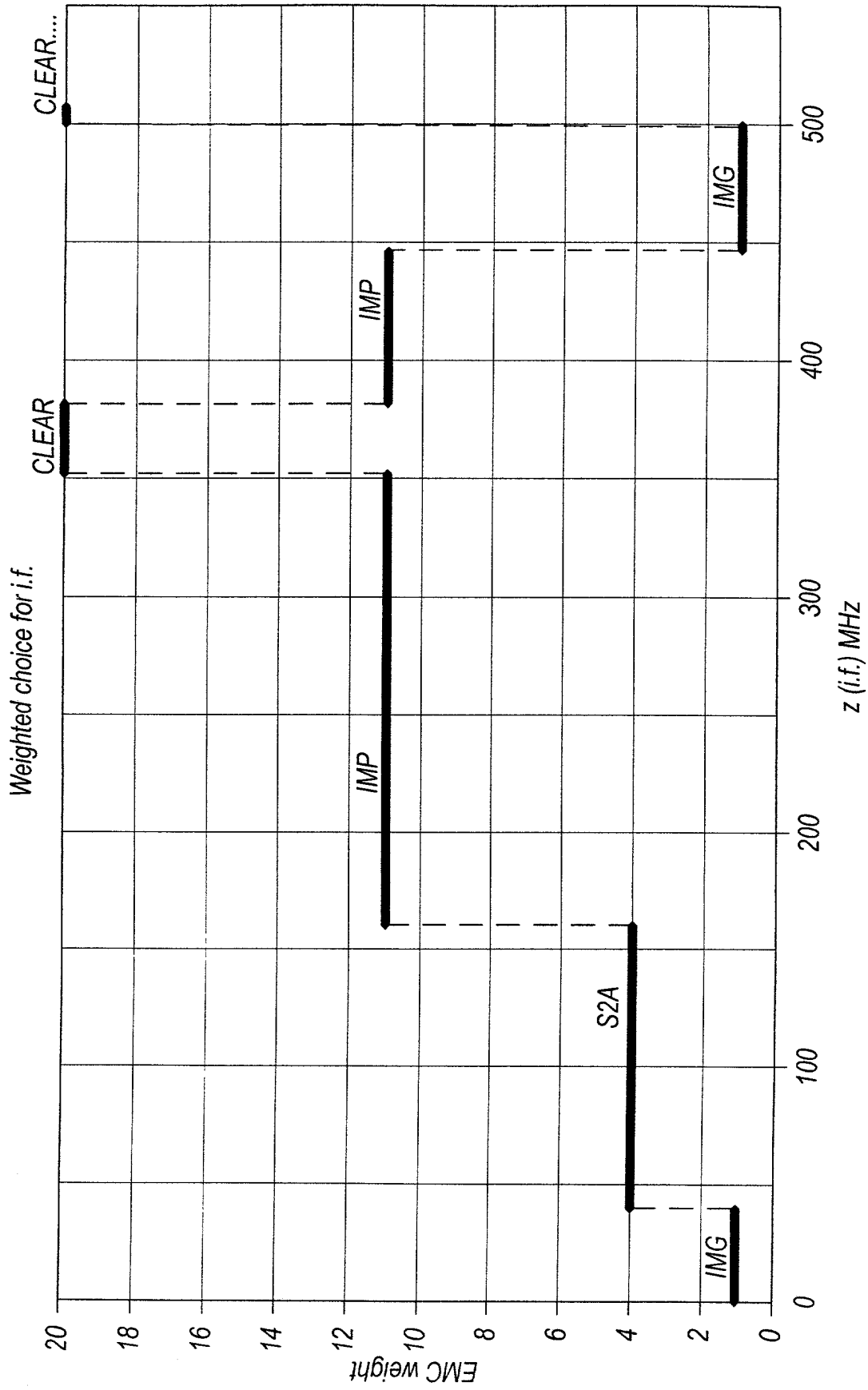


Fig.7

8/12

Composite-analysis (4 EMC Templates)

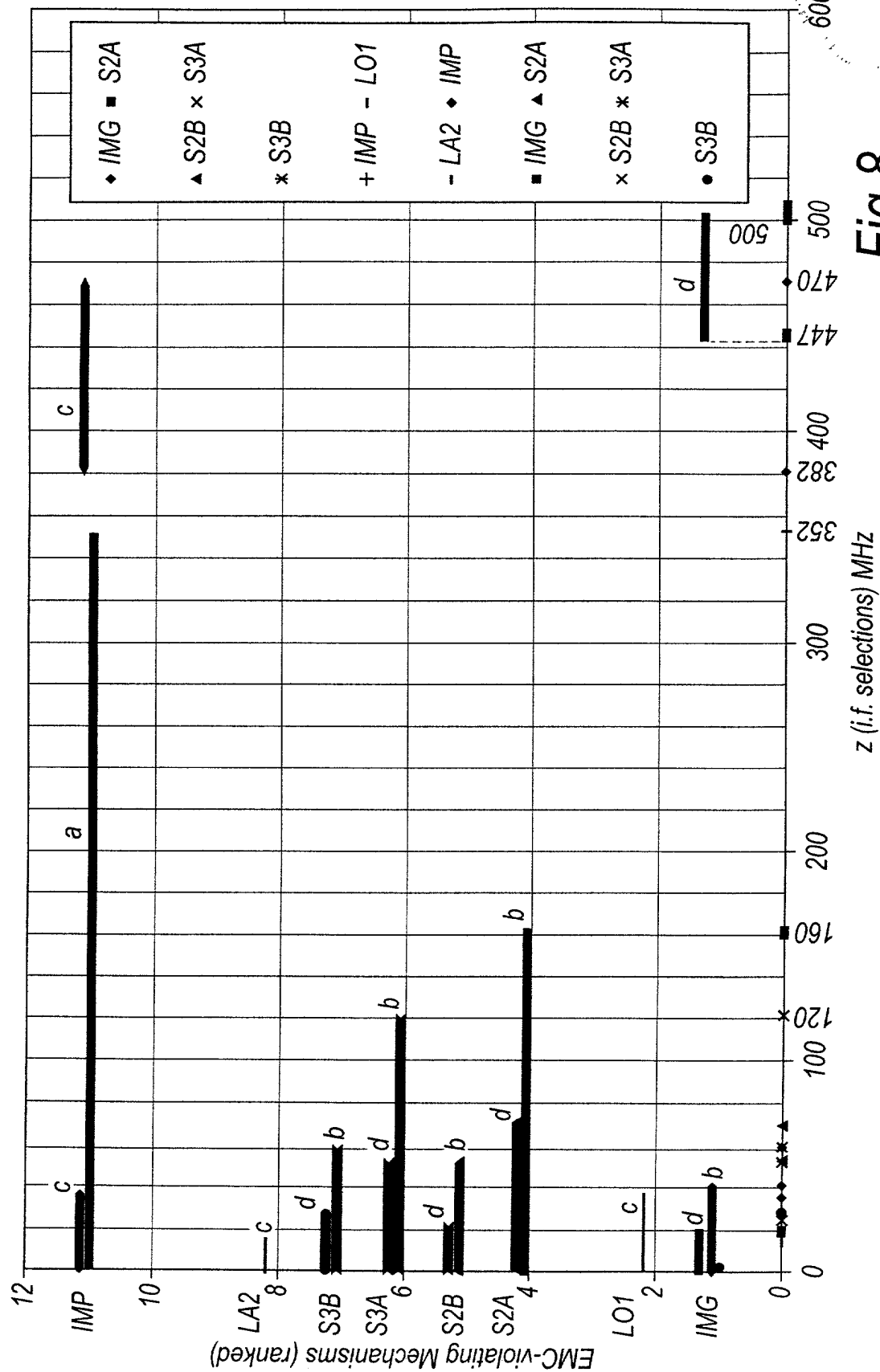


Fig.8

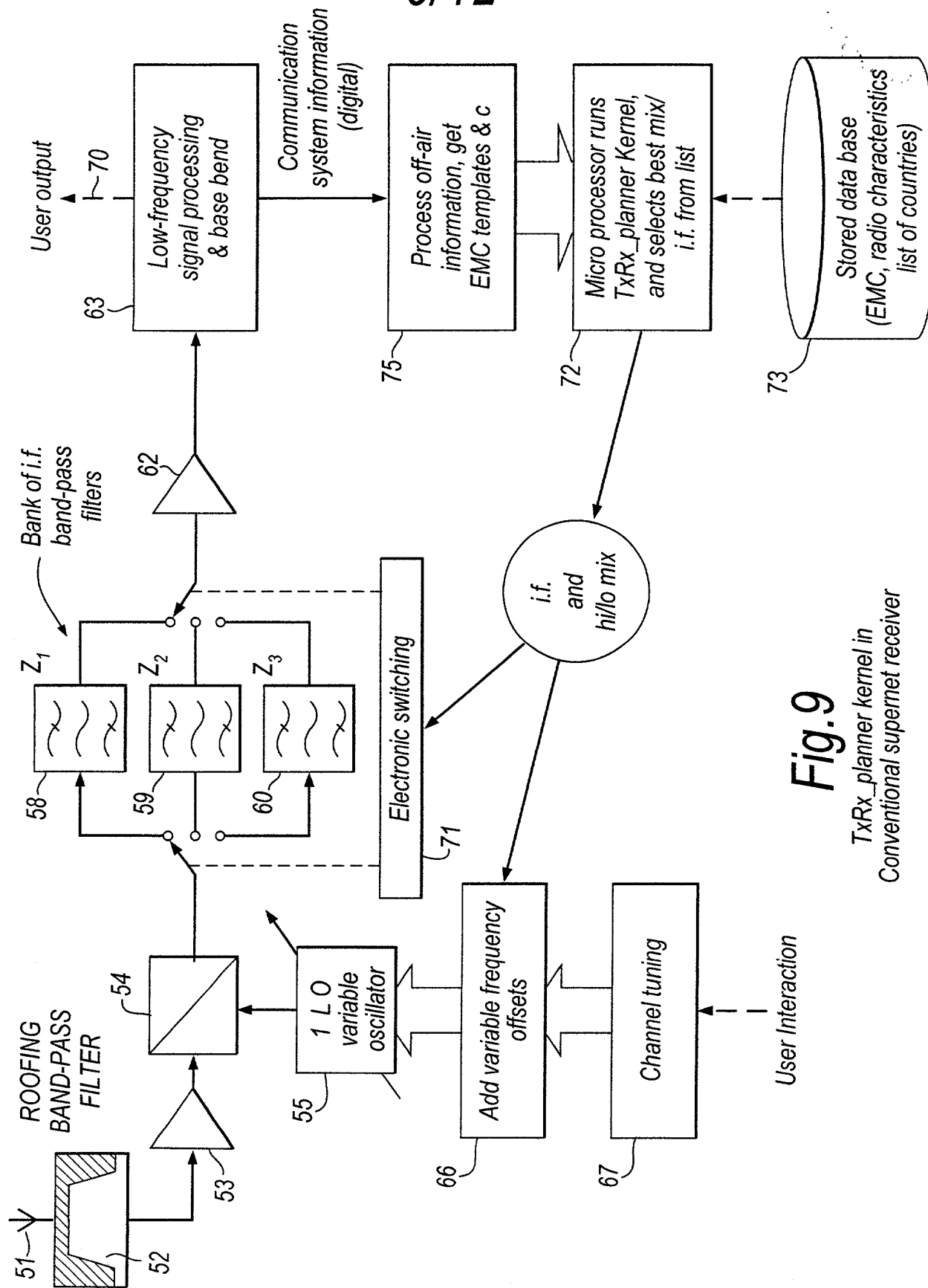


Fig.9

TxRx_planner kernel in
Conventional supernet receiver

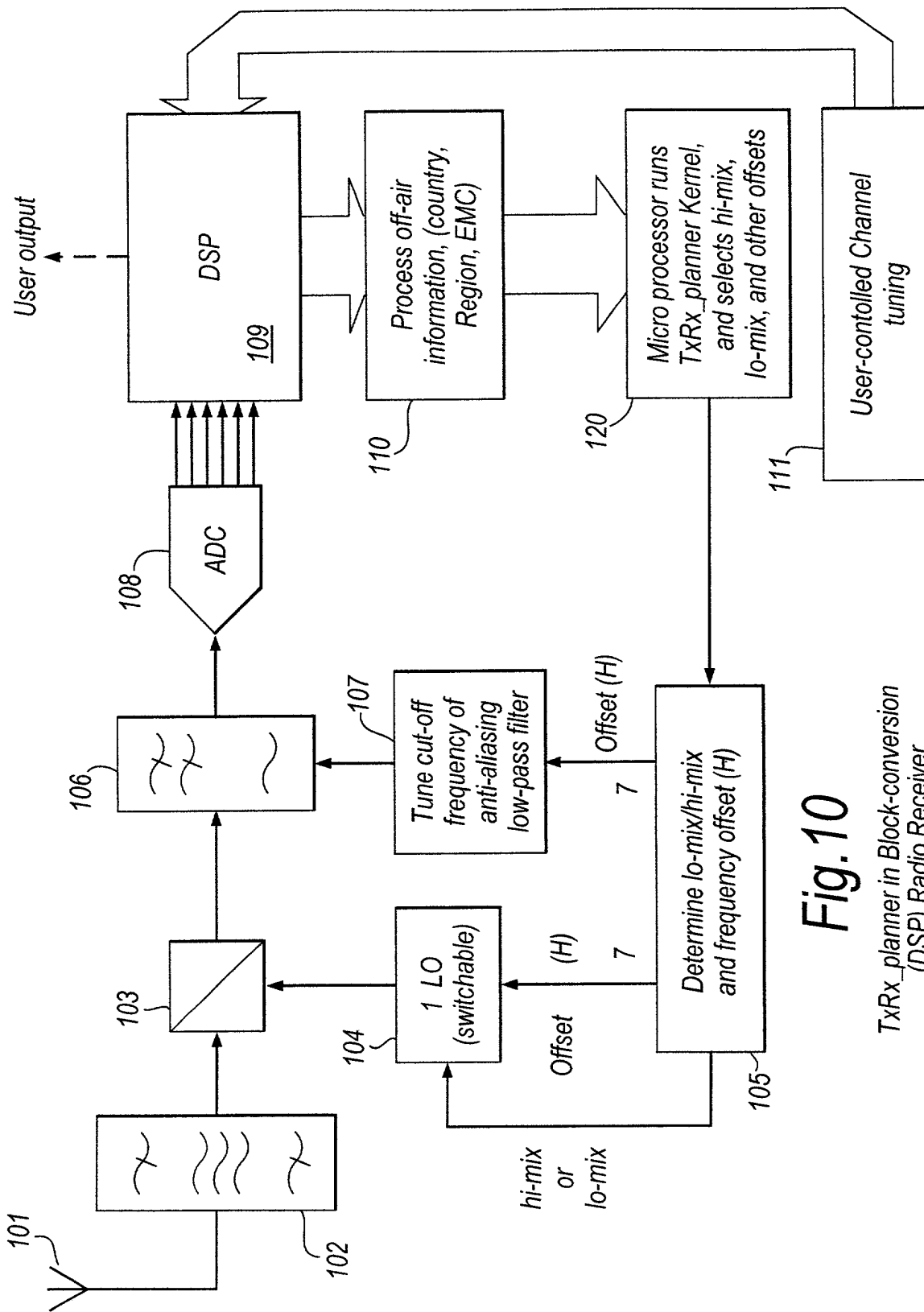
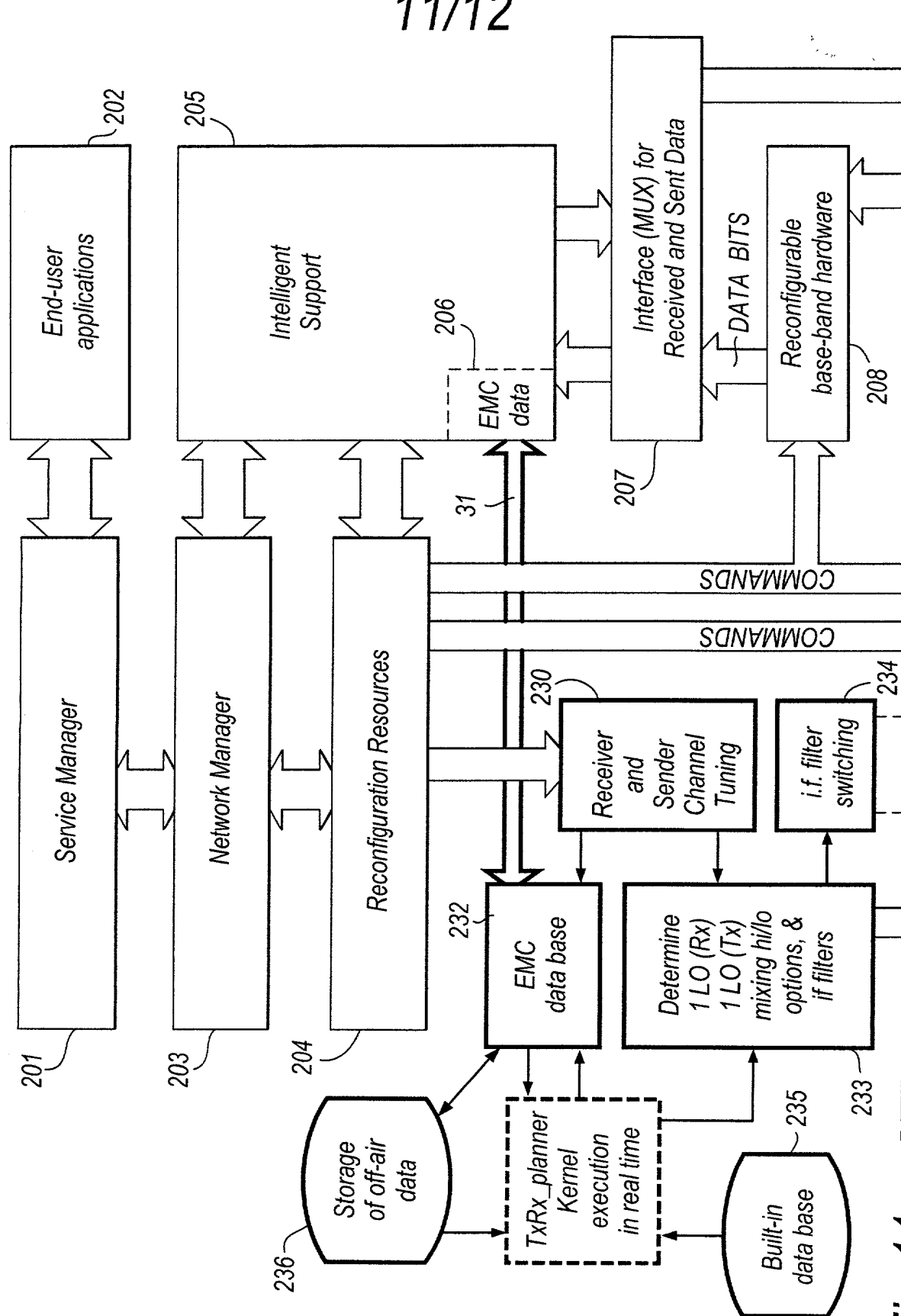


Fig.10

TxRx_planner in Block-conversion (DSP) Radio Receiver



12/12

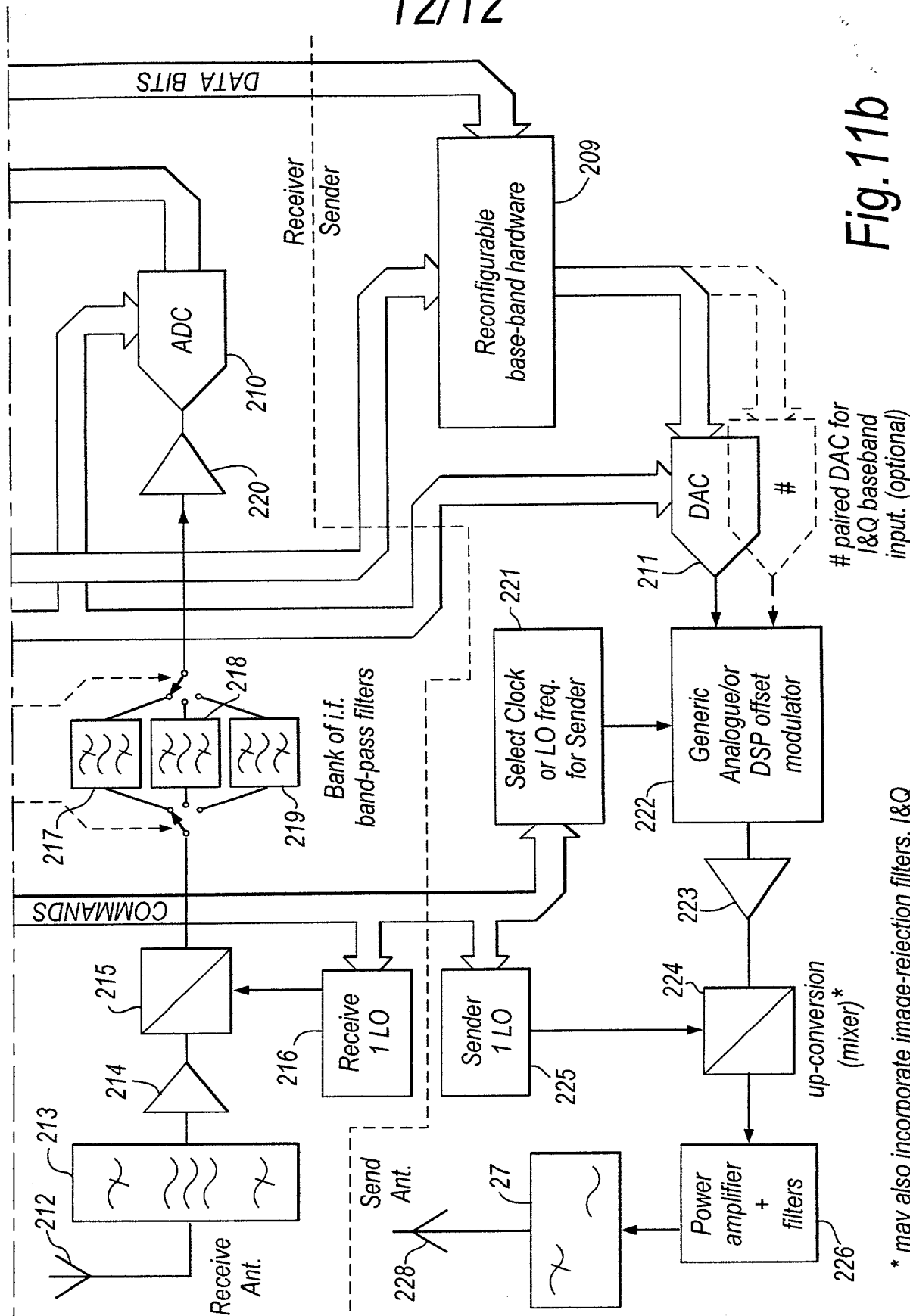


Fig. 11b

* may also incorporate image-rejection filters, I&Q image-suppression frequency changer, etc.